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APPLICATION THAT MET THE REQUIREMENTS TO BE GRANTED A
FILING DATE.

APPLICATION NUMBER: 10/403,147

FILING DATE: March 31, 2003

RELATED PCT APPLICATION NUMBER: PCT/US03/10354

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1049 U.S. PTO

UTILITY PATENT APPLICATION TRANSMITTAL
(Large Entity)

(Only for new nonprovisional applications under 37 CFR 1.53(b))

Docket No.
0247.00016Total Pages in this Submission
22**TO THE ASSISTANT COMMISSIONER FOR PATENTS**Box Patent Application
Washington, D.C. 20231

Transmitted herewith for filing under 35 U.S.C. 111(a) and 37 C.F.R. 1.53(b) is a new utility patent application for an invention entitled:

VAPOR-ASSISTED CRYOGENIC CLEANING

and invented by:

Souvik Banerjee; Harlan Forrest Chung

1049 U.S. PTO
10/403147If a **CONTINUATION APPLICATION**, check appropriate box and supply the requisite information:☐ Continuation ☐ Divisional ☐ Continuation-in-part (CIP) of prior application No.: 60/369,852

Which is a:

☐ Continuation ☐ Divisional ☐ Continuation-in-part (CIP) of prior application No.: _____

Which is a:

☐ Continuation ☐ Divisional ☐ Continuation-in-part (CIP) of prior application No.: _____

Enclosed are:

Application Elements

1. ☒ Filing fee as calculated and transmitted as described below
2. ☒ Specification having 13 pages and including the following:
 - a. ☒ Descriptive Title of the Invention
 - b. ☒ Cross References to Related Applications (if applicable)
 - c. ☐ Statement Regarding Federally-sponsored Research/Development (if applicable)
 - d. ☐ Reference to Sequence Listing, a Table, or a Computer Program Listing Appendix
 - e. ☒ Background of the Invention
 - f. ☒ Brief Summary of the Invention
 - g. ☒ Brief Description of the Drawings (if filed)
 - h. ☒ Detailed Description
 - i. ☒ Claim(s) as Classified Below
 - j. ☒ Abstract of the Disclosure

UTILITY PATENT APPLICATION TRANSMITTAL (Large Entity)

(Only for new nonprovisional applications under 37 CFR 1.53(b))

Docket No.
0247.00016

Total Pages in this Submission
22

Application Elements (Continued)

3. ☒ Drawing(s) (when necessary as prescribed by 35 USC 113)
 - a. ☒ Formal Number of Sheets 2
 - b. ☐ Informal Number of Sheets _____
4. ☒ Oath or Declaration
 - a. ☒ Newly executed (original or copy) ☐ Unexecuted
 - b. ☐ Copy from a prior application (37 CFR 1.63(d)) (for continuation/divisional application only)
 - c. ☒ With Power of Attorney ☐ Without Power of Attorney
 - d. ☐ DELETION OF INVENTOR(S)
Signed statement attached deleting inventor(s) named in the prior application, see 37 C.F.R. 1.63(d)(2) and 1.33(b).
5. ☐ Incorporation By Reference (usable if Box 4b is checked)
The entire disclosure of the prior application, from which a copy of the oath or declaration is supplied under Box 4b, is considered as being part of the disclosure of the accompanying application and is hereby incorporated by reference therein.
6. ☐ CD ROM or CD-R in duplicate, large table or Computer Program (Appendix)
7. ☐ Application Data Sheet (See 37 CFR 1.76)
8. ☐ Nucleotide and/or Amino Acid Sequence Submission (if applicable, all must be included)
 - a. ☐ Computer Readable Form (CRF)
 - b. ☐ Specification Sequence Listing on:
 - i. ☐ CD-ROM or CD-R (2 copies); or
 - ii. ☐ Paper
 - c. ☐ Statement(s) Verifying Identical Paper and Computer Readable Copy

Accompanying Application Parts

9. ☐ Assignment Papers (cover sheet & document(s))
10. ☐ 37 CFR 3.73(B) Statement (when there is an assignee)
11. ☐ English Translation Document (if applicable)
12. ☐ Information Disclosure Statement/PTO-1449 ☐ Copies of IDS Citations
13. ☐ Preliminary Amendment
14. ☒ Return Receipt Postcard (MPEP 503) (Should be specifically itemized)
15. ☐ Certified Copy of Priority Document(s) (if foreign priority is claimed)
16. ☒ Certificate of Mailing

☐ First Class ☒ Express Mail (Specify Label No.): EV255774592US

UTILITY PATENT APPLICATION TRANSMITTAL
(Large Entity)*(Only for new nonprovisional applications under 37 CFR 1.53(b))*Docket No.
0247.00016Total Pages in this Submission
22**Accompanying Application Parts (Continued)**

17. ☐ Additional Enclosures *(please identify below):*

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Request That Application Not Be Published Pursuant To 35 U.S.C. 122(b)(2)

18. ☐ Pursuant to 35 U.S.C. 122(b)(2), Applicant hereby requests that this patent application not be published pursuant to 35 U.S.C. 122(b)(1). Applicant hereby certifies that the invention disclosed in this application has not and will not be the subject of an application filed in another country, or under a multilateral international agreement, that requires publication of applications 18 months after filing of the application.

Warning

An applicant who makes a request not to publish, but who subsequently files in a foreign country or under a multilateral international agreement specified in 35 U.S.C. 122(b)(2)(B)(i), must notify the Director of such filing not later than 45 days after the date of the filing of such foreign or international application. A failure of the applicant to provide such notice within the prescribed period shall result in the application being regarded as abandoned, unless it is shown to the satisfaction of the Director that the delay in submitting the notice was unintentional.

UTILITY PATENT APPLICATION TRANSMITTAL (Large Entity)

(Only for new nonprovisional applications under 37 CFR 1.53(b))

Docket No.
0247.00016

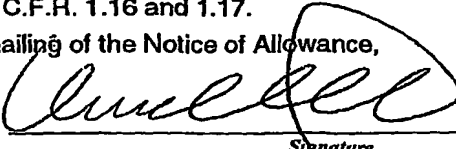
Total Pages in this Submission
22

Fee Calculation and Transmittal

CLAIMS AS FILED

For	#Filed	#Allowed	#Extra	Rate	Fee
Total Claims	18	- 20 =	0	x \$18.00	\$0.00
Indep. Claims	2	- 3 =	0	x \$84.00	\$0.00
Multiple Dependent Claims (check if applicable) <input type="checkbox"/>					\$0.00
BASIC FEE					\$750.00
OTHER FEE (specify purpose)					\$0.00
TOTAL FILING FEE					\$750.00

- ☒ A check in the amount of \$750.00 to cover the filing fee is enclosed.
- ☐ The Commissioner is hereby authorized to charge and credit Deposit Account No. 11-1449 as described below. A duplicate copy of this sheet is enclosed.
- ☐ Charge the amount of as filing fee.
- ☒ Credit any overpayment.
- ☒ Charge any additional filing fees required under 37 C.F.R. 1.16 and 1.17.
- ☐ Charge the issue fee set in 37 C.F.R. 1.18 at the mailing of the Notice of Allowance, pursuant to 37 C.F.R. 1.311(b).


Signature

Dated: March 31, 2003

Kenneth L. Kohn, Reg. No. 45,791
KOHN & ASSOCIATES, PLLC
30500 Northwestern Highway, Suite 410
Farmington Hills, Michigan 48334
(248) 539-5050

cc:

VAPOR-ASSISTED CRYOGENIC CLEANING

[0001] The present application claims priority from U.S. patent application no. 60/369,852, filed on April 5, 2002.

FIELD OF THE INVENTION

[0002] The present invention relates to the use of a reactive gas or vapor of a reactive liquid, with or without a free radical generator, and either simultaneously or sequentially, with cryogenic cleaning to aid in the removal of foreign materials (FM), e.g. particles, films, and other contaminants, from semiconductor surfaces and other surfaces involved in precision cleaning.

BACKGROUND OF THE INVENTION

[0003] The demands for greater switching speed and circuit performance have seen the advent of new dielectric materials (dielectric constant of <3) and metals to reduce the RC delay constant in circuits. The metal of choice, which is copper, has added several challenges to the process integration scheme. For aluminum interconnects, the metal patterning was performed by reactive ion etching (RIE) of the aluminum followed by dielectric deposition. With copper, the dielectric film is first deposited and etched to form vias and trenches followed by the deposition of copper in those etched features. The excess copper is then removed using chemical mechanical polishing (CMP) to planarize the surface for subsequent layers of film. This method of forming copper interconnects for the back-end-of-line (BEOL) is known as the Dual Damascene process.

[0004] Following the dielectric etch to form the vias and trenches, a large amount of fluoropolymeric residue is left both on the surface of the wafer and on the inside of features as seen in Figure 1. These residues are generated during the etching process, partly for sidewall passivation during anisotropic etching. The etch residue has to be cleaned prior to the deposition of the successive film layers: the copper barrier Ta/TaN

film, copper seed layer, and finally the electrochemical filling of the features with copper in the Damascene process.

[0005] The dimensions of the features used in the interconnects at the BEOL are currently around $0.13 \mu\text{m}$. For cryogenic cleaning to work effectively in removing the sidewall residues from inside the features, as shown in Figure 1, the cryogenic particles must be less than $0.13 \mu\text{m}$ in size. As well, these particles must arrive at the surface of the wafer with enough velocity to impart the momentum transfer required to dislodge the sidewall residue.

[0006] There are three mechanisms by which surface cleaning is done: 1) momentum transfer by cryogenic particles to overcome the force of adhesion of slurry particles to the wafer surface, 2) drag force of the cleaning gases to remove the dislodged particles off the surface of the wafer, and 3) the dissolution of organic contaminants by liquid formed at the interface of the cryogenic particle and the wafer surface.

[0007] In CO_2 cryogenic cleaning, liquid CO_2 at a pressure of about 850 psi from a purified source is made to expand through the orifice of a specially designed nozzle intended to make the expansion a constant enthalpy process. The expansion of liquid CO_2 through the nozzle creates solid and gaseous CO_2 in a highly directional and focused stream. Due to the gas flow over the wafer surface, a boundary layer is formed. The CO_2 cryogenic particles must travel through the boundary layer to arrive at the wafer surface and at the contaminant particle to be removed. During the flight through the boundary layer, their velocity decreases due to the drag force on them by the gaseous CO_2 in the boundary layer. Assuming the thickness of the boundary layer to be h , a snow particle must enter the layer with a normal component of velocity equal to at least h/t where t is the time taken to cross the boundary layer and arrive at the wafer surface. The relaxation time of the particle crossing the boundary layer is given in equation (1) as the following:

$$\tau = \frac{2a^2\rho_p C_e}{9\eta} \quad (1)$$

where:

a is the particle radius

ρ_p is the particle density

η is the viscosity of the gas

C_c is the Cunningham slip correction factor given as in equation (2)

$$C_c = 1 + 1.246(\lambda/a) + 0.42(\lambda/a)\exp[-0.87(a/\lambda)] \quad (2)$$

where λ is the mean free path of gas molecules. Since the CO₂ cryogenic cleaning is conducted at atmospheric pressure, the Cunningham slip correction factor becomes equal to 1 in equation (1) for cryogenic particles larger than 0.1 μm in size.

[0008] Thus, for CO₂ snow particles to have sufficient momentum to remove foreign material from the wafer surface and from inside the features, the time to cross the boundary layer must be less than the relaxation time, in which case they will arrive at the surface with greater than 36% of the initial velocity. Equation 1 shows that the relaxation time decreases with particle size. Therefore, the smaller-sized particles will not be able to arrive at the wafer surface with sufficient velocity to effectively clean the inside walls of the submicron vias and trenches.

[0009] The prior art processes generally use CO₂ or argon cryogenic spray for removing foreign material from surfaces. As examples, see U.S. Patent No. 5,931,721 entitled Aerosol Surface Processing; U.S. Patent No. 6,036,581 entitled Substrate Cleaning Method and Apparatus; U.S. Patent No. 5,853,962 entitled Photoresist and Redeposition Removal Using Carbon Dioxide Jet Spray; U.S. Patent No. 6,203,406 entitled Aerosol Surface Processing; and U.S. Patent No. 5,775,127 entitled High Dispersion Carbon Dioxide Snow Apparatus. In all of the above prior art patents, the foreign material is removed from a relatively planar surface by physical force involving momentum transfer to the contaminants. However, such cleaning methods are inadequate for features with high aspect ratios such as in vias and trenches in the back-end-of-line integrated device fabrication process where removal of small submicron particles and complex polymeric residues, as generated by dielectric etch processes, is required.

[0010] U.S. Patent No. 6,332,470 entitled Aerosol Substrate Cleaner discloses the use of vapor only or vapor in conjunction with high pressure liquid droplets for cleaning

semiconductor substrate. Unfortunately, the liquid impact does not have sufficient momentum transfer capability as solid CO₂ and will therefore not be as effective in removing the smaller-sized particles. U.S. Patent No. 5,908,510 entitled Residue Removal by Supercritical Fluids discloses the use of cryogenic aerosol in conjunction with supercritical fluid or liquid CO₂. Since CO₂ is a non-polar molecule, the solvation capability of polar foreign material is significantly reduced. Also, since the liquid or supercritical CO₂ formation requires high pressure (greater than 75 psi for liquid and 1080 psi for supercritical), the equipment is expensive. U.S. Patent No. 6,231,775 proposes the use of sulfur trioxide gas by itself or in combination with other gases for removing organic materials from substrates as in ashing. Such vapor phase cleaning is inadequate for removing cross-linked photoresist formed during the etching in a typical dual Damascene integration scheme using low k materials such as carbon doped oxides.

[0011] As such, there remains a need for the effective and efficient removal of homogeneous and inhomogeneous contaminants consisting of cross-linked and bulk photoresist, post-etch residues, and sub-micron sized particulates both from the surface of the wafer as well as from inside high aspect ratio features.

BRIEF DESCRIPTION OF THE FIGURES

[0012] Embodiments of the present invention are described with reference to the figures in which:

Figure 1 shows the cleaning of the post-trench etch residues in a dual-damascene structure. The left image is the SEM of the post-trench etch structure with etch residues present. The right image is the SEM of the post-trench etch structure after a sequence of plasma and wet clean steps.

Figure 2 shows a schematic diagram of a conventional CO₂ cryogenic cleaning system.

SUMMARY OF THE INVENTION

[0013] The invention comprises the use of a reactive gas or the vapor of a reactive liquid which can diffuse into the high aspect ratio features or through a layer of contaminants film and chemically react with the foreign material. Cryogenic cleaning will also be used, either sequentially or simultaneously, with the reactive gas or vapor cleaning to remove the contaminants and films from the surfaces and from inside patterned features of semiconductors and other substrates requiring precision cleaning of their surfaces.

DETAILED DESCRIPTION

[0014] The present invention comprises the use of a reactive gas or the vapor of a reactive liquid either simultaneously or sequentially with cryogenic cleaning. The reactive gas or vapor used in the process of the present invention is selected according to its reactivity with the contaminants on the substrate surface. After reacting with these contaminants, it also preferably produces byproducts in a gaseous form. (Hereinafter, for ease of reference in the description of the present invention, references to reactive gas may include reactive vapors of a liquid and references to reactive vapors may include reactive gases.) The reactive gas or vapor preferably stays in contact with the substrate surface for up to 20 minutes, preferably less than 10 minutes, and more preferably less than 2 minutes.

[0015] Examples of the vapor of a reactive liquid which may be used in the present process may be the vapor of a high vapor pressure liquid and include, but not limited to, acetone, ethanol-acetone mixtures, isopropyl alcohol, methanol, methyl formate, methyl iodide, and ethyl bromide. It may also be another gas such as ozone, water vapor, hydrogen, nitrogen, nitrogen oxides, nitrogen trifluoride, helium, argon, neon, sulfur trioxide, oxygen, fluorine, or fluorocarbon gases or combinations of gases. The gas or vapor should be reactive with the organic photoresist as well as the fluoropolymer etch residue inside the features. As well, the reaction byproducts are preferably gaseous so

that they can be removed from the cleaning chamber by the flow of nitrogen gas. Preferred gases and vapors include isopropyl alcohol, ethanol-acetone mixtures, methanol, ozone, water vapor, nitrogen trifluoride, sulfur trioxide, oxygen, fluorine and fluorocarbon gases.

[0016] In post-etch cleaning applications, cryogenic particles cannot get inside the high aspect ratio features of vias and trenches. Vapor is needed to diffuse into these features effectively. The vapor will then chemically react with the polymeric residue and convert it to gaseous by-products which can be removed from the surface by a flow of nitrogen across the substrate surface. Alternatively, the vapor can be introduced in a separate chamber kept under low pressure. The vapor phase reaction in this chamber could be done at temperatures of up to 200°C. Following the vapor clean, the wafers may be transferred to a second cleaning chamber at atmospheric pressure where the cryogenic cleaning can take place.

[0017] During the process, the vapor may condense on the wafer surface. With the proper choice of vapors, the condensation could also lower the Hamaker constant and hence the force of adhesion of particles to surfaces. This condensation would thereby help in the particle removal by the CO₂ cryogenic cleaning.

[0018] In semiconductor wafer cleaning processes, the foreign material to be removed includes not only particle contaminants but also films of organic, inorganic, and metal-organic residues at various steps in microelectronic manufacturing both in FEOL (front-end-of-line) and BEOL processes. These films cannot be removed by purely physical mechanisms. Chemical assistance to any physical mechanism of removal is required to meet cleanliness requirements. In the present invention, the gas phase cleaning is the chemical means of cleaning whereas the cryogenic cleaning is predominantly the physical mechanism of cleaning. The two processes working in tandem or in sequence are able to completely remove the homogeneous or inhomogeneous foreign materials.

[0019] The reactivity of the gas or vapor of a reactive liquid with the contaminants may be further increased using a free radical initiator such as ultra violet light, X-ray, Excimer laser, corona discharge or plasma to generate reactive chemical species. It is

combined with the physical cleaning of snow or cryogenic aerosols to remove the non-reactive foreign material. Similar cleaning mechanisms are seen in wet cleaning and dual frequency plasma cleaning using downstream MW plasma to generate the chemical species for reaction with the contaminant and RF plasma to generate the ion bombardment.

[0020] In one embodiment of the present invention in combination with CO₂ cryogenic cleaning, the vapor of a reactive liquid is sprayed through a nozzle attached to the same arm as a CO₂ cryogenic nozzle. The nozzle may be a small stainless steel bore, 1/4 to 1/2" in diameter, or a specially designed nozzle with corona wire along the axis to initiate discharges in the vapor. The nozzle is preferably at an angle of approximately 10°-90° to the substrate surface. The vapor may also be sprayed through a showerhead positioned above the substrate surface to ensure uniform coverage of the substrate surface. During the vapor delivery, the substrate is preferably kept at the same temperature as the vapor. If condensation of the vapor is desired, the substrate may be kept at a temperature below the vapor to initiate condensation of the vapor into a thin film of liquid on the substrate surface. However, if desired, the vapor may be made reactive with the assistance of a free radical initiator such as ultraviolet light, x-ray, excimer laser, corona discharge, or plasma. This step is generally included in the process when the vapor is not sufficiently reactive for a given contaminant type. The vapor is sprayed onto the substrate surface for preferably up to twenty minutes. It may be sprayed continuously or intermittently. Preferably, a single type of vapor is used but a mixture of vapors may be used simultaneously or sequentially, if preferred, to remove particular foreign materials.

[0021] Following the application of vapor, the CO₂ cryogenic cleaning is performed. Cryogenic cleaning is well known within the industry and any well known technique may be used. A standard CO₂ cryogenic cleaning process is described in U.S. Patent No. 5,853,962 which is incorporated herein by reference. As an example of a typical CO₂ cryogenic cleaning system, reference is made to Figure 2. The cleaning container 12 provides an ultra clean, enclosed or sealed cleaning zone. Within this cleaning zone is the wafer 1 held on a platen 2 by vacuum. The platen with wafer is kept at a controlled

temperature of up to 100°C. Liquid CO₂, from a cylinder at room temperature and 850 psi, is first passed through a sintered in-line filter 4 to filter out very small particles from the liquid stream to render the carbon dioxide as pure as possible and reduce contaminants in the stream. The liquid CO₂ is then made to expand through a small aperture nozzle, preferably of from about 0.05" to 0.15" in diameter. The rapid expansion of the liquid causes the temperature to drop resulting in the formation of solid CO₂ snow particles entrained in a gaseous CO₂ stream flowing at a rate of approximately 1-3 cubic feet per minute. The stream of solid and gaseous CO₂ is directed at the wafer surface at an angle of about 30° to about 60°, preferably at an angle of about 45°. The nozzle is preferably positioned at a distance of approximately 0.375" to 0.5" measured along the line of sight of the nozzle to the wafer surface. During the cleaning process, the platen 2 moves back and forth on track 9 in the y direction while the arm of the cleaning nozzle moves linearly on the track 10 in the x direction. This results in a rastered cleaning pattern on the wafer surface of which the step size and scan rate can be pre-set as desired. The humidity in the cleaning chamber is preferably maintained as low as possible, for example, <-40°C dew point. The low humidity is present to prevent the condensation and freezing of water on the wafer surface from the atmosphere during the cleaning process which would increase the force of adhesion between the contaminant particles and the wafer surface by forming crystalline bridges between them. The low humidity can be maintained by the flow of nitrogen or clean dry air.

[0022] Throughout the cleaning process, it is important that the electrostatic charge in the cleaning chamber be neutralized. This is done by the bipolar corona ionization bar 5. The system also has a polonium nozzle mounted directly behind the CO₂ nozzle for enhancing the charge neutralization of the wafer which is mounted on an electrically grounded platen. The electrostatic charge develops by triboelectrification due to the flow of CO₂ through the nozzle and across the wafer surface. It is aided by the low humidity maintained in the cleaning chamber.

[0023] For particulate contaminants, the removal mechanism is primarily by momentum transfer of the CO₂ cryogenic particles to overcome the force of adhesion of the contaminant particles on the wafer surface. Once the particles are "loosened", the

drag force of the gaseous CO₂ removes them from the surface of the wafer. In contrast, the cleaning mechanism for organic film contaminants is by the formation of a thin layer of liquid CO₂ at the interface of the organic contaminants and the surface due to the impact pressure of the cryogenic CO₂ on the wafer surface. The liquid CO₂ can then dissolve the organic contaminants and carry it away from the wafer surface.

[0024] The spraying of the gas or vapor in accordance with the present invention may occur in the same chamber as the cryogenic cleaning or it may be done in a separate chamber. As well, the cryogenic cleaning may be initiated simultaneously with or directly after the reactive gas or vapor is used. Depending on the gas or vapor used, for example water vapor, it may be desirable to purge the chamber of this vapor prior to initiating the cryogenic cleaning.

[0025] As a result of the use of the reactive gas or vapor, the removal of contaminants, particularly from etched features on a substrate surface, is significantly improved. This cleaning method is particularly beneficial in removing homogeneous contaminants such as a film of post etch residue on the sidewalls of vias and trenches or the photoresist remaining after etching.

[0026] The embodiments and examples of the present invention are meant to be illustrative of the present invention and not limiting. Other embodiments which could be used in the present process would be readily apparent to a skilled person. It is intended that such embodiments are encompassed within the scope of the present invention.

WHAT IS CLAIMED IS:

1. A process for the removal of contaminants from a substrate surface requiring precision cleaning, comprising the steps of: a) applying at least one reactive gas or vapor of a reactive liquid to the substrate surface to react with the contaminants on the substrate surface; and b) cryogenically cleaning the surface of the substrate; to remove substantially all of the contaminants from the substrate surface.
2. The process of claim 1 wherein steps a) and b) are carried out simultaneously.
3. The process of claim 2 wherein steps a) and b) are carried out sequentially.
4. The process of claim 1 wherein the at least one vapor of a reactive liquid is selected from one or more of the group of liquids consisting of ethanol, acetone, ethanol-acetone mixtures, isopropyl alcohol, methanol, methyl formate, methyl iodide, ethyl bromide, and combinations thereof.
5. The process of claim 1 wherein the at least one reactive gas is selected from one or more of the group consisting of ozone, water vapor, hydrogen, nitrogen, nitrogen oxides, nitrogen trifluoride, helium, argon, neon, sulfur trioxide, oxygen, fluorine, fluorocarbon gases and combinations thereof.
6. The process of claim 1 wherein the at least one reactive gas or vapor is selected from the group consisting of isopropyl alcohol, ethanol-acetone mixtures, methanol, ozone, water vapor, nitrogen trifluoride, sulfur trioxide, oxygen, fluorine and fluorocarbon gases.
7. The process of claim 1 wherein the reactive gas or vapor remains in contact with the surface for up to 20 minutes prior to the initiation of cryogenic cleaning.

8. The process of claim 1 wherein the contaminants are less than $0.13\ \mu\text{m}$ in size.
- 5 9. A process of cleaning the surface of a semiconductor to remove contaminants comprising the steps of: a) applying at least one reactive gas or vapor of a reactive liquid to the surface of the substrate for reacting with the contaminants, thereby forming gaseous byproduct; b) keeping the at least one reactive gas or vapor in contact with the surface for up to 20 minutes; c) removing the gaseous byproducts; and c) cryogenically cleaning the surface.
- 10 10. The process of claim 9 wherein the at least one reactive gas or vapor is introduced in a chamber under low pressure and/or at temperatures of up to 200°C .
11. The process of claim 10 wherein removing the byproducts in step (c) comprises
15 purging the chamber with nitrogen or clean dry air.
12. The process of claim 11 wherein the byproducts are removed by passing a flow of nitrogen over the substrate surface.
- 20 13. The process of claim 9 wherein the at least one reactive gas or vapor is applied to the surface in the presence of a free radical initiator to generate reactive chemical byproducts from the reactive gas and the contaminants.
14. The process of claim 13 wherein the free radical initiator is ultraviolet light, x-ray,
25 laser, corona discharge, or plasma.
15. The process of claim 9 wherein the at least one vapor of a reactive liquid is selected from one or more of the group of liquids consisting of ethanol, acetone, isopropyl alcohol, methanol, methyl formate, methyl iodide, ethyl bromide, and
30 mixtures thereof.

16. The process of claim 9 wherein the at least one reactive gas is selected from one or more of the group consisting of ozone, water vapor, hydrogen, nitrogen, nitrogen oxides, nitrogen trifluoride, helium, argon, neon, sulfur trioxide, oxygen, fluorine, fluorocarbon gases and mixtures thereof.
17. The process of claim 9 wherein the contaminants are about 0.13 μm in size or less.
18. The process of claim 9 wherein the step of cryogenically cleaning the surface comprises the steps of spraying a liquid CO_2 stream through a nozzle to form a gaseous CO_2 stream having solid CO_2 particles, and directing said stream at the surface; thereby removing substantially all of the contaminants from the surface.

ABSTRACT

The present invention is directed towards the use of a reactive gas or vapor of a reactive liquid prior to or in combination with cryogenic cleaning to remove contaminants from the semiconductor surfaces or other substrate surfaces requiring precision cleaning. The reactive gas or vapor is selected according to the contaminants to be removed and the reactivity of the gas or vapor with the contaminants. Preferably, this reaction forms a gaseous byproduct which is removed from the substrate surface by the flow of nitrogen across the surface.

Docket No.
0247.00016

Declaration and Power of Attorney For Patent Application

English Language Declaration

As a below named inventor, I hereby declare that:

My residence, post office address and citizenship are as stated below next to my name,

I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled
VAPOR-ASSISTED CRYOGENIC CLEANING

the specification of which

(check one)

- ☒ is attached hereto.
- ☐ was filed on _____ as United States Application No. or PCT International
Application Number _____
and was amended on _____
(if applicable)

I hereby state that I have reviewed and understand the contents of the above identified specification, including the claims, as amended by any amendment referred to above.

I acknowledge the duty to disclose to the United States Patent and Trademark Office all information known to me to be material to patentability as defined in Title 37, Code of Federal Regulations, Section 1.56.

I hereby claim foreign priority benefits under Title 35, United States Code, Section 119(a)-(d) or Section 365(b) of any foreign application(s) for patent or inventor's certificate, or Section 365(a) of any PCT International application which designated at least one country other than the United States, listed below and have also identified below, by checking the box, any foreign application for patent or inventor's certificate or PCT International application having a filing date before that of the application on which priority is claimed.

Prior Foreign Application(s)

Priority Not Claimed

_____ (Number)	_____ (Country)	_____ (Day/Month/Year Filed)	<input type="checkbox"/>
_____ (Number)	_____ (Country)	_____ (Day/Month/Year Filed)	<input type="checkbox"/>
_____ (Number)	_____ (Country)	_____ (Day/Month/Year Filed)	<input type="checkbox"/>

<u>60/369,852</u>	<u>04-05-02</u>
(Application Serial No.)	(Filing Date)

(Application Serial No.) _____ (Filing Date) _____

(Application Serial No.) _____ (Filing Date) _____

I hereby claim the benefit under 35 U. S. C. Section 120 of any United States application(s), or Section 365(c) of any PCT International application designating the United States, listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States or PCT International application in the manner provided by the first paragraph of 35 U.S.C. Section 112, I acknowledge the duty to disclose to the United States Patent and Trademark Office all information known to me to be material to patentability as defined in Title 37, C. F. R., Section 1.56 which became available between the filing date of the prior application and the national or PCT International filing date of this application:

(Application Serial No.)	(Filing Date)	(Status) (patented, pending, abandoned)
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[illegible]

(Application Serial No.)	(Filing Date)	(Status) (patented, pending, abandoned)

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POWER OF ATTORNEY: As a named inventor, I hereby appoint the following attorney(s) and/or agent(s) to prosecute this application and transact all business in the Patent and Trademark Office connected therewith. *(list name and registration number)*

Kenneth L. Kohn, Reg. No. 30,955

Amy E. Rinaldo, Reg. No. 45,791

Andrew M. Farial, Reg. No. 50,382

Send Correspondence to: Kenneth L. Kohn
KOHN & ASSOCIATES, PLLC
30500 Northwestern Highway, Suite 410
Farmington Hills, Michigan 48334

Direct Telephone Calls to: *(name and telephone number)*
Kenneth L. Kohn (248) 539-5050

Full name of sole or first inventor BANERJEE, Souvik	03/26/03 Date
Sole or first inventor's signature <i>Souvik Banerjee</i>	
Residence 4458 Gibraltar Drive, Fremont, California 94536	
Citizenship US	
Post Office Address Same as above	

Full name of second inventor, if any CHUNG, Harlan Forrest	3/26/03 Date
Second inventor's signature <i>Harlan F. Chung</i>	
Residence 16848 Columbia Drive, Castro Valley, California 94552	
Citizenship US	
Post Office Address Same as above	

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FIGURE 1

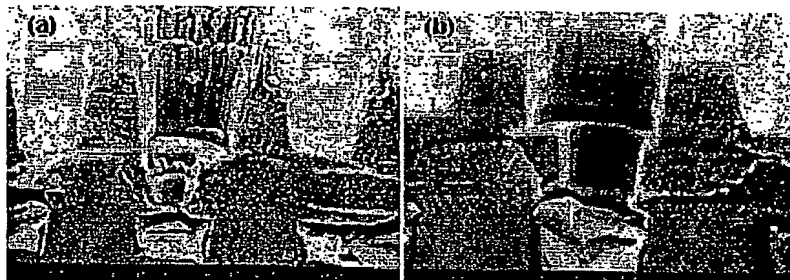
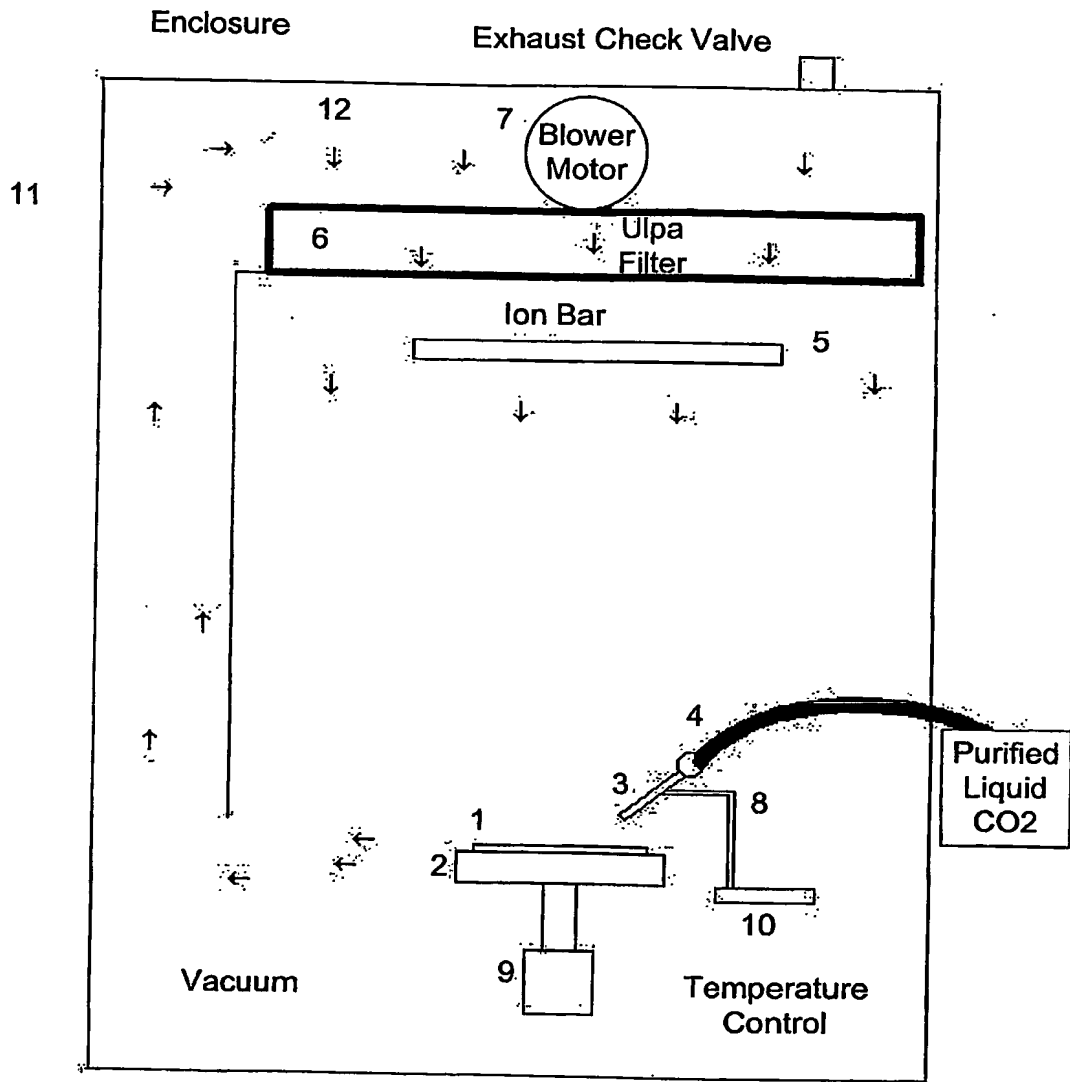


Figure 1. Source: Semiconductor International, July 1, 2001

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FIGURE 2



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